List of Component in Electrical Engineering Department

Sr.no.	Name of Component	20	Resistors Variable 1 Watt 1k Ohm wire wound
1	MOSFET'S (N-Channel) IRLZ34N	21	Resistors Variable 1 Watt 10k Ohm wire wound
2	MOSFET'S (N-Channel) 2N700	22	Resistors Variable 1 Watt 100k Ohm wire wound
3	MOSFET'S (N-Channel) IRLB8721	23	Resistors Variable 1 Watt 47k Ohm wire wound
4	MOSFET'S (N-Channel) IRLZ44N/IRFZ44N	24	Resistors Variable 1 Watt 1M Ohm wire wound
5	MOSFET'S (N-Channel) IRLML6402	25	Capacitors (ceramic) 27nF
6	MOSFET'S (N-Channel) SI2301	26	Capacitors (ceramic) 47nF
7	MOSFET'S (N-Channel) STP55PF06/STPNF06;P	27	Capacitors (ceramic) 68nF
8	BJT'S (NPN) 2SA1943	28	Capacitors (ceramic) 100nF
9	Diodes (Schottky) SS34	29	Capacitors (electrolytic) 1uf, 50V
10	Diodes (Schottky) MBR20100	30	Capacitors (film & tantalum) 10uF 50V
11	Diode (Brigde Rectifier) MB6S	31	Capacitors (film & tantalum) 22uF, 50V
12	Voltage regulator LM1117	32	Capacitors (film & tantalum) 27uF, 50 V
13	Voltage regulator LM2596	33	Capacitors (film & tantalum) 47uF, 50 V
14	Voltage regulator XL6009	34	Capacitors (film & tantalum) 68uF, 50 V
15	Motor & power control IC's DRV8833	35	Capacitors (film & tantalum) 82uF, 50 V
16	Motor & power control IC's TB6612FNG	36	Inductors 1uH
17	Microcontrollers & processors RASBERRRY Pi 4B	37	Wireless Modules NRF24L01
18	Microcontrollers & processors RASBERRRY RP2040	38	Wireless Modules LoRaSX1278
19	Microcontroller board STM32	39	Wireless Modules SIM800L

Use these components for projects.

Head
Department of Electrical Engineering
SGGS Institute of Engineering & Tecnology
Vishnupuri, Nanded - 431 606 (M.S.)

Department of Electrical Engineering

NOTICE

Subject: Mini and Major Project Assignments for 10th August 2024

All faculty members of the Electrical Engineering Department are hereby informed that the process of assigning mini and major projects for the academic year 2024-25 will take place on 10th August 2024, the whole day.

Key points to note:

1. Project Assignment:
    - Each faculty member has the right to choose one group from the third year and one group from the final year for their projects.
    - Students also have the right to choose their project guide. Both the group and the faculty member must mutually agree on the selection.
2. Literature Review Presentation:
    - Students will present a literature review of their chosen area of work as part of the project initiation process. This will help in assessing the scope and feasibility of the project.
3. Responsibilities:
    - Faculty members are requested to facilitate the selection process and ensure that students are guided appropriately in choosing their projects.
    - All project work should align with the academic and research goals of the department and contribute to the overall development of the students' practical skills.

Head of the Department
Electrical Engineering Department
SGGSIET Nanded

1) J

1) Jamraj Satish. S —
2) Nayan S. Bisue —
3) Seema pachpute —
4) G.R. Bame

SHRI GURU GOBIND SINGHJI INSTITUTE OF ENGINEERING AND TECHNOLOGY, NANDED
DEPARTMENT OF ELECTRICAL ENGINEERING

NOTICE

All faculty members of the Electrical Engineering Department are hereby informed to submit their book/journal/software requirements for the academic year 2025–26 in the prescribed format attached below.

Kindly ensure that the requirements are relevant to the current curriculum, laboratory work, or ongoing research activities. The duly filled forms should be submitted to the department office on or before 31st July 2025.

This data will be consolidated and forwarded to the central library for further processing and procurement.

29/7/25

Head, Electrical Engineering Department
SGGSIET, Nanded

Sr. No.	Name of Faculty Member	Signature
1	Dr. Paramjeet Singh Jamwal	26/07/2025
2	Dr. Srinivas Nagaballi	26/07/25
3	Mr. Satish Jamraj Sir	29/07/25
4	Miss. Seema Pachpute Madam	
5	Mr. Harshawardhan Bhavthankar Sir	
6	Mr. Mangesh Rahul Pande Sir	

Department of Electrical Engineering
Shri Guru Gobind Singhji Institute of Engineering & Technology, Nanded

Date: 09/09/2025

Application for Project Proposal – Hardware Development (2025–2026)

The Department of Electrical Engineering invites applications from students (individual or group, from all years) for hardware-based project development.

The aim is to promote innovation, solve institute-related problems, and build industry-relevant skills.

Project Proposal Requirements

Each proposal must include:

1. Problem Statement
2. Objectives
3. Literature Review (brief)
4. Timeline for prototype development (till April 2026)
5. Detailed and justified budget
6. Industrial relevance and student learning outcome

Project Duration & Evaluation

1. The total project duration will be till April 2026.
2. Monthly evaluation will be carried out by the department.
3. If linked to academic credits:
   → Progress in the current semester will be assessed.
   → Final grading will be based on completion of the project in the following semester.

Presentation & Approval

1. Students/groups must make a presentation before the departmental project committee (DPC).
2. Financial support will be approved only after explaining the need for hardware and feasibility.
3. The final decision will be based on the DPC all three-member approval.
4. Maximum budget will be based on the nature of the project which will be approved by DPC.

Suggested Problem Areas (Institute-Focused)

1. Detecting voltage between transformer neutral and earth near EMC meter room.
2. Automatic phase sequence changer (to protect 3-phase loads when MSEDCL feeder sequence changes).
3. Automatic load calculation and display system for each building.
4. Any other automation/improvement for existing campus issues related to solar, DG, panels etc.

Benefits and Recommendations

- Projects aligned with institute problems will be highly appreciated.
- Students will gain practical industry-oriented exposure in hardware design, budgeting, and project management.
- The initiative will help in building hands-on engineering competence relevant to industrial requirements.

Submission Deadline: By the end of September 2025.
Submit Proposals To: Head, Department of Electrical Engineering

Prof. S. M. Gudhe
Head of Department
Department of Electrical Engineering
SGGSIET, Nanded

Departmental Project Committee (DPC)

1	Head of the Department	Chairman
2	Dr. P S Jamwal	Member
3	Dr. S Nagaballii	Member

Existing Available Hardware/Instrument

1. High accurate industry Multimeter/LCR Meter/Three Phase Power Analyser/High voltage clamp meter/Differential Voltage Probe/High Sampling DSO.
2. PCB Machine for Circuit Prototype.
3. Microcontrollers with high sampling time.
4. Digital Signal Processor development board (TMS320F28335).
5. Three Phase Inverter Stack (two level and NPC).
6. High Voltage 15KVA lab autotransformers.

SHRI GURU GOBIND SINGHJI INSTITUTE OF ENGINEERING & TECHNOLOGY, NANDED

Department of Electrical Engineering

Policy for Continuous Evaluation of Practical Work (As per AICTE Norms)

In accordance with AICTE guidelines on Continuous Internal Evaluation (CIE), the following policy shall be implemented for all practical courses in the Department of Electrical Engineering.

1. Number of Practical's: Each student shall complete not less than 8 practical's during the semester.

2. Viva-Voce Evaluation

   1. For every practical performed in each week, the viva-voce will be conducted in the next practical slot.
   2. Each viva will be of 10 marks.
   3. The minimum marks awarded shall be 5 for attempting the viva. Marks between 5 and 10 will be given based on the student's performance in subject knowledge, body language, communication and general etiquettes

3. Absence Policy.

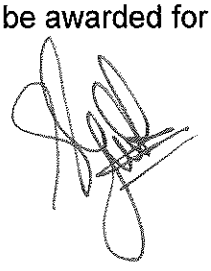
   1. If a student is absent for the practical or viva, zero (0) marks will be awarded.
   2. No exceptions will be made for absence.
   3. Students may improve their overall grade by performing well in subsequent practical's.

4. Responsibility and Transparency

   1. The Practical Coordinator will evaluate each viva.
   2. Students must sign against their awarded marks as acknowledgment.
   3. Students are expected to reflect on feedback and seek improvement.

5. Final Grading

   1. The final practical grade will be the cumulative average of all Vivas conducted throughout the semester.
   2. There will be no external practical examination.
   3. This ensures that marks remain fully in the student's control, based on consistent performance.
   4. The overall practical grading will be based on total marks out of 80 or 100 and will be normalized to 100-point scale.
   5. Grades will be awarded for practical course as follows:



Grade	Grade Range
A+	90 ≤100
A	80≤90
B+	70≤80
B	60≤70
C+	50≤60
C	45≤50
D	39≤45
F	0≤ 39

6. The grade will be multiplied by the grade point as shown below:

Grades	Grade Point
A+	10
A	9
B+	8
B	7
C+	6
C	5
D	4
F	0

7. For each practical course, the pointer contribution is calculated as *Credits × Grade Point.* For example, in a 1-credit practical, if a student secures grade 'B' (7 grade points), then the pointer earned for that practical will be 1 × 7 = 7. (This calculation is only for that individual practical and should not be confused with the overall semester pointer, which is computed using SGPA formula given in rules and regulations of the institute)

This policy is effective and is to be strictly followed.

Head of Department
Electrical Engineering
SGGSIET, Nanded

Head
Department of Electrical Engineering
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