

Reg. No.	Name of student	Title of Thesis
2014MEC001	Rachapwar Shivraj Shyamsundar	Low power high speed hybrid 1-bit full adder circuit on 45nm technology
2014MEC002	Shaha Pratik Sudhir	Monocular camera calibration and depth estimation for intelligent
2014MEC003	Tatkar Nikhil Ashokrao	Design and analysis of block of SSD controller
2014MEC004	Ojha Veena	ASIC clock tree conversion for FPGA based prototyping
2014MEC005	Agrawal Milind Gopal	Area efficient precise clock generation for USB 2.0
2014MEC006	Attharkar Nikheel Vijaykumar	Low power low voltage 10 bit SAR ADC for industrial application
2014MEC007	Darve Ratnamala Devidas	Design of low cost harmonic analyzer using TMS32
2014MEC008	Potdar Abhijit Shashikant	Design and layout of bidirectional GPIO for multiple loads
2014MEC009	Jadhav Gaurav Vinayak	Production quality ETM model generation delivering fast turnaround while lowering the cost of design and licenses
2014MEC010	Patil Diksha	Development of video processing board using FPGA for higher format thermal images
2014MEC011	Kakde Dnyaneshwar Bhagwat	Power optimization in 8T SRAM cell
2014MEC012	Mallabdkar Atique Ab Munaf	ECG processing for predicting ventricular arrhythmia
2014MEC013	Deshmukh Dhananjay Arun	Design of successive approximation ADC
2014MEC015	Ankushe Supriya Sureshrao	New approach of comparision for fast ICA non-gaussianity methods
2014MEC016	Sonawale Pankaj Chandrakant	Design and simulation of reflect array antenna for highly directive beam
2014MEC017	Parekar Janabai Sitaram	Automatic retinal image blood vessel segmentation and registration using retinal fundus images
2014MEC018	Rathod Sandhya Shivajirao	Implementation of low power high speed Viterbi decoder