Programme Educational Objectives (PEOs)

PEO I: To study the physics of semiconductor device technology and develop proficiency in computational methods for advanced modeling and simulation (preparation).

PEO II: To study signal and image processing concepts (Core competence) and to design embedded and VLSI systems (Core competence).

PEO III: To study and design digital, analog, and mixed signal VLSI systems (Breadth). Understand the state of art in the recent areas of research in signal and image processing, pattern recognition and computer vision techniques (Breadth).

PEO IV: Provide academic environment aware of excellence, leadership, and ethical codes to students; and teach them lifelong learning skills including research component needed for successful professional career (Learning environment).

Program Outcomes (POs)

PO1: Acquire knowledge of signal processing, image processing and pattern recognition concepts.

PO2: Acquire knowledge of Computer Vision, Artificial Neural Networks, multimedia systems and applications and telecommunication engineering concepts.

PO3: Acquire knowledge of soft computing and VLSI design concepts.

PO4: Demonstrate an ability to identify, formulate and solve electronics engineering problems.

PO5: Demonstrate an ability to design analog, digital and mixed-signal VLSI electronic circuits and conduct experiments with such electronic systems, analyze and interpret data.

PO6: Demonstrate an ability to visualize and work on laboratory and multidisciplinary tasks.

PO7: Demonstrate skills to use modern engineering tools such as wavelets, HDLs, Embedded C and Scilab etc.

PO8: Demonstrate the capability to design and develop embedded systems to solve modern day problems.

PO9: Be capable of succeeding in competitive examinations for further studies such as Doctor of Philosophy (Ph.D.) or research-oriented entrance examinations.

PO10: Develop confidence for self-education and analysis of the complex engineering problem.

PO11: Be aware of contemporary societal issues and will show the understanding of impact of electronics engineering solutions.

PO12: Demonstrate proficiency in both verbal and written form