

Brief Report of the One day workshop on **Industry Institute Interaction and Partnership Embedded and VLSI Domain**

Following experts participated in the workshop

- Mr. Vivek Pawar, CEO, Sankalp Semiconductor, Pvt. Ltd., Hubli
- Mr. Pradeep Dharne, Managing Director, Applied Micro Circuits, Pune
- Mr. Sachin Pathak, Mentor Graphics, Bangalore
- Mr. Ravi Boonapalli, Mentor Graphics, Bangalore
- Mr. DVR Murthy, SocTronics Technologies, Hyderabad
- Mr. Gangadhar Gude, Invecas, Hyderabad
- Mr. Shivraj Dharane, Intel Corporation, Bangalore
- Mr. Shivraj Thakare, Intel Corporation, Bangalore
- Mr. Vishwas Kurundkar, Eklakshya R&D, Pune
- Mr. Rajiv Mantri, Eklakshya R&D, Pune
- Mr. Satish Deshpande, Eklakshya R&D, Pune

The workshop was attended by two local industrialists Mr. Deepak Mortale and Mr. Murli Mohan and UG and PG students and faculty of the Institute. More than 100 participants registered for the workshop.

The Workshop was inaugurated by Dr. L. M. Waghmare, Director, SGGS IE&T, Nanded. Prof. Y. V. Joshi elaborated on the objectives of the workshop. Prof. Waghmare in his speech assured all the support in establishment of VLSI and Embedded Center of Excellence at SGGS IE&T, Nanded with the help of participation from Industries working in the domain. In the inaugural function, MoU was signed between SGGS IE&T, Nanded and Eklakshya VLSI R&D, Pune for collaborative activities of training in VLSI and Embedded domain.

Mr. Vivek Pawar stressed the need of self learning as a requirement for employment. Mr. Pradeep Dharne elaborated efforts done by Applied micro in bringing Industry and institute for building an eco system. Mr. Gangadhar Gude has showcased Invecas and the opportunities in the field.

Mr. DVR Murthy elaborated the national scenario on research and development in the field and funding of different Indian and US universities. Mr. Sachin Pathak and Mr. Ravi Boonapalli elaborated on the efforts Mentor Graphics is doing in development of linkage between Universities and Industries.

Mr. Shivraj Dharne and Mr. Shivraj Thakre gave the detailed requirements of Industries and skill sets required for becoming employable.

Mr. Satish Deshpande spoke on opportunities in Embedded systems. Mr. Rajiv Mantri has given a motivational speech and motivated students in taking up

career in VLSI and Embedded domain. Prof. Y. V. Joshi concluded the workshop by highlighting the major achievements.

After the workshop, a meeting to finalise the curriculum for M. Tech. In Embedded systems and VLSI Design was held in the presence of the Director. It was attended by Prof. R. S. Holambe, Prof. J. V. L Venkatesh (Dean and secretary of Alumni Association), alongwith other faculty members from Engineering colleges in the vicinity.



Prof. Y. V. Joshi –Introductory speech giving the objectives of the Workshop



Director Dr. L. M. Waghmare inaugurating the workshop



Mr. Vivek Pawar, Executive Chairman, Sankalp Semiconductor Pvt. Ltd. Hubli during his presentation



Signing of MOU between Eklakshya VLSI R&D Pune and SGGS IE&T, Nanded



Signed by Director of SGGSIE&T, Nanded Dr. L. M. Waghmare and CEO, Eklakshya Mr. Vivek Pawar



Mr. Pradeep Dharne, MD, Applied Micro Circuits, Pune



Mr. Gangadhar Gude, Invecas, Hyderabad



Audience of the Workshop