

CURRICULUM VITAE

Name : *Jonnalagedda Megha Venkatesh (PhD)*
(Maiden name: Megha P. Salkade)

Date of birth : 11-12-1970

Designation : Associate Professor,
Department of Information Technology, SGGSIE&T, Nanded

Professional Aims

- a. To be a leading professional in the field of VLSI and Embedded System Design.
- b. To create and provide conducive learning environment for students.
- c. Help students in all-round development and placement.

Academic Qualifications :

Degree	University	Field of Specialization	Year
Ph.D	Swami Ramanand Teerth Marathawada University, Nanded	Electronics Engineering (Sign Language Interpretation)	2016
M.E.	Swami Ramanand Teerth Marathawada University, Nanded	Electronics Engineering (with specialization in <i>Computer Technology</i>)	2000
B.E.	Marathawada University, Aurangabad	Electronics Engineering	1993

Membership of Professional Bodies

- Life member of Institution of Engineers (India)
- Life member, IET
- IEEE member during 2012-13
- CSI Member during 2012-2014

Employment record (starting from present position)

Period		Institute	Designation
From	To		
07/07/2010	Till date	SGGS Institute of Engineering and Technology, Nanded	Associate Professor, IT Department

17/06/2009	07/07/2010	SGGS Institute of Engineering and Technology, Nanded	Associate Professor, EXTC Department
07/08/2006	30/04/2009	Aurora's Technological and Research Institute, Hyderabad	Associate Professor
01/01/2004	10/06/2006	Mahatma Gandhi Mission's College of Engineering, Nanded	Assistant Professor
15/07/1996	31/12/2003	Mahatma Gandhi Mission's College of Engineering, Nanded	Lecturer
11/08/1995	14/07/1996	Mahatma Gandhi Mission's College of Engineering, Nanded	Lecturer

Total Teaching Experience 23 years

Administrative Experience 4 years

Subjects Taught

Sr. No.	Subject Taught	UG/PG
1.	Electronic Devices & Circuits (I & II)	UG
2.	Digital Integrated Circuits & Analysis	UG
3.	Digital Signal Processing	UG
4.	VLSI Design	UG
5.	Embedded Systems	UG
6.	Digital VLSI Design	PG
7.	Low Power VLSI Design	PG
8.	Microprocessor and interfacing	UG
9.	Digital System Design	UG
10.	Research Methodology	PG
11.	Ethics in IT	UG
12.	Multimedia Systems	UG
13.	Network and Lines	UG
14.	Basic Electronics	UG

Courses / Conferences Organized

Sr. No.	Title of the course	Institute	Duration
01	Short term training program on VLSI design using Tanner tools	Aurora's Technological and Research Institute, Hyderabad	Four days
02	Three days workshop on designing using FPGA	Aurora's Technological and Research Institute, Hyderabad	Three days
03	National Conference on Advances in Computing, Networking and Security.	Shri Guru Gobind Singhji Institute of Engg. and Technology, Nanded	Two days 23-24 Dec. 2013
04	Seven days workshop on 'Communication and interview Skills development'	Shri Guru Gobind Singhji Institute of Engg. and Technology, Nanded	During Semester - I, 2014-15
05	One week TEQIP II sponsored STTP on "Data Mining using STATISTICA"	Shri Guru Gobind Singhji Institute of Engg. and Technology, Nanded	05-10 July, 2016 (Six Days)
06	One day workshop on Digital Design using XILINX	Shri Guru Gobind Singhji Institute of Engg. and Technology, Nanded	13 th August, 2016
07	A Statistical Approach to Research Methodology	Shri Guru Gobind Singhji Institute of Engg. And Technology, Nanded	Six days 20 th to 25 th August, 2018

Courses Attended

Sr. No.	Title of the course	Institute	Duration in weeks
01	Digital Signal and Image Processing	SGGSCE&T, Nanded	2
02	Microprocessors in Instrumentation	SGGSCE&T, Nanded	3
03	Systems Engineering Techniques & Applications	SGGSCE&T, Nanded	3
04	Programming with C and C++	SGGSCE&T, Nanded	2
05	VLSI Design using VHDL	SGGSCE&T, Nanded	2
06	Recent Trends in Signal and Image Processing	SGGSIE&T, Nanded	3 days
07	Low Power VLSI Design	MIST, Hyderabad	2 days
08	SDP on Mixed Signal VLSI Design	AEC, Hyderabad	2

09	SDP on Advances in Video & Image Processing	SGGSIE&T, Nanded	2
10	Workshop on Nanotechnology	SGGSIE&T, Nanded	1
11	Workshop on Research Methodology	SGGSIE&T, Nanded	2 days
12	TEQIP-II sponsored FDP on "Wireless Sensors and their Applications"	SGGSIE&T, Nanded	1 week
13	TEQIP-II sponsored FDP on "Emerging Trends in Nanoscience and Nanotechnology"	SGGSIE&T, Nanded	1 week
14	TEQIP-II sponsored on day workshop on "Rethinking, Teaching and Learning"	SGGSIE&T, Nanded	1 day
15	TEQIP-II sponsored FDP on "Computer Vision and Pattern Recognition"	SGGSIE&T, Nanded	3 weeks
16	TEQIP-II sponsored FDP on "Product Lifecycle Management"	SGGSIE&T, Nanded	1 week
17	Two day workshop on "High Impact Presentations"	Dale Carnigie & Associates, Mumbai	2 days

PG Projects Guided

1. **Number of Projects** - 16
2. **Titles of major projects guided**
 - i. RF Transmitter and Receiver Design using **Cadence**
 - ii. Biometric system for employee attendance

Publication

1. *M. P. Salkade, D. D. Doye, Dr. T. R. Sontakke, "Time Alignment Algorithm for Speech Recognition System", at International Symposium on Information Technology, Peoples' Development and culture, Indian Academy of Social Sciences & J. K. Institute of Applied Physics and Technology, University of Allahabad, Allahabad, February 12 – 16, 2001.*
2. *Megha V. Jonnalagedda and Dharmpal D. Doye, "Radially defined Local Binary Patterns for Facial Expression Recognition", International Journal of Computer Applications(0975 – 8887), Vol.119, No.2, pp 17-22, June 2015.*
3. *Megha V. Jonnalagedda , J. S. Padmaja and Dharmpal D. Doye, "Radially Defined Local Binary Patterns for Hand Gesture Recognition", ICEEC 2015, 24th August, 2015 held in Kochin and later published in International Journal of Advanced Trends in Computer Science and Engineering (IJATCSE), Vol.4 , No.4, pp 44 - 48 (2015), ISSN 2278 – 3091*
4. *Megha V. Jonnalagedda and Dharmpal D. Doye, "Augmented local binary patterns for facial expression recognition", published in - **International Journal of Mathematics and***

Computation, CESER publications, ISSN 0974-570X (online), ISSN 0974-5718 (Print), Vol. 27; Issue No. 3, pp 59 - 68, 2016.

5. *Megha V. Jonnalagedda and Dharmpal D. Doye*, "A simple and efficient approach to extract features for facial expression recognition", published in - **International Journal of Mathematics and Computation, CESER** publications, ISSN 0974-570X (online), ISSN 0974-5718 (Print), Vol. 27; Issue No. 4, pp 57 - 67, 2016.
6. *Megha V. Jonnalagedda and Dharmpal D. Doye*, "Facial expression recognition using radially defined pixel intensity augmented local binary patterns", under review - *Springerplus*.
7. *Vaibhav Bhosale and Megha Jonnalagedda*, "Survey on Frauds in Financial Transactions", *International Journal of Computer Science and Information Technology*, Vol. 5 (3), 2014, 4711-4713, ISSN:0975-9646
8. *Prashant Mandale and J. V. Megha*, "Segmentation of MRI Brain Images and Improve Execution time and Performance for Automatic Diagnosis", *Indian Conference on Signal and Image Processing - 2015 (ICONSIP-2015)*, (sponsored by Centre of Excellence - Signal and Image Processing), held during 10-11 July, 2015 at Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded, Maharashtra.
9. *Mussavir Shaikh and J. V. Megha*, "A Biometric Modality based on typing behavior", *Indian Conference on Signal and Image Processing - 2015 (ICONSIP-2015)*, (sponsored by Centre of Excellence - Signal and Image Processing), held during 10-11 July, 2015 at Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded, Maharashtra.
10. *Abhay Jadhav and J. V. Megha*, "User Identity Prediction by Mouse Gesture Dynamics through ANN", *International Conference on Recent Innovations in Engineering, Science and Technology (ICRIEST)*, held by IRAJ Research Forum in association with Institute of Research and Journals, on 1stFebruary, 2015, Pune.
11. *Abhay Jadhav and J. V. Megha*, "User Authentication by Mouse Signature - A survey Paper", *Indian Conference on Signal and Image Processing - 2015 (ICONSIP-2015)*, (sponsored by Centre of Excellence - Signal and Image Processing), held during 10-11 July, 2015 at Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded, Maharashtra.
12. *Sushmita Magdum and J. V. Megha*, "Mining online Reviews and Tweets for Predicting Sales Performance and Success of Movies", *International Conference on Intelligent Computing and Control Systems (ICICCS 2017)*, organized by Vaigai College of Engineering during June 15-16, 2017 at Madurai, India, ISSN:978-1-5386-2745-7

13. *Pramod Waghmare and J. V. Megha, "Efficient Pattern Recognition in Time Series Data", 2nd International Conference on Inventive Computational Technologies(ICICT-2017), organized by RVS Technical Campus during August 24-25, 2017 at Coimbatore, India.*
14. *Yugandhara Yeotikar and J. V. Megha, "Dynamic Multiple Choice Questions Generation from Domain Ontology", Accepted for publication in 2nd International Conference on Inventive Computing Tech., India., 2017.*

Institute and University level activities

- a. Developed VLSI Laboratory at the Institute.
- b. Member of Academic Cell meant for inculcating academic discipline in the institute.
- c. Worked as Chief Superintendent for University Examination (thrice).
- d. Member, Board of Studies (Electronics & Telecommunication Engineering) at Swami Ramanand Teerth Marathawada University, Nanded.
- e. Member, 32/5 committee (theory & practical examination related planning work) at Swami Ramanand Teerth Marathawada University, Nanded.
- f. Worked as departmental in charge for preparing NBA proposal at MGM's COE, Nanded, ATRI, Hyderabad and SGGSI&T, Nanded.
- g. Prepared MODROBS proposal for EXTC and IT departments at SGGSI&T, Nanded which got approved. (Proposal worth 21 lakhs (17 + 6) got sanctioned.)
- h. Executive Member of SGGSI Engineers' Alumni Association (SEAA) – a Charitable Trust since 2010 till Dec. 2015
- i. Played a pivotal role in organizing Alumni Meet – 2011 at SGGSI&T, Nanded.
- j. Worked as Departmental T&P Coordinator (from 2010 to 2013) for IT department at SGGSI&T, Nanded.
- k. Worked as one of the Faculty Advisor (Sponsorship), for PRAGYAA - a National level Technical Symposium at SGGSI&T, Nanded.
- l. Worked as Faculty In-charge for the Institute Magazine – DRISHTI at SGGSI&T, Nanded, which won first prize at SRTM University, Nanded.
- m. Currently holding the post for Head, IT Department since July 2013.
- n. In an effort to increase Industrial tie ups, signed up a MoU with Laurus Infosystem, Bangalore, through which provided Internship for 12 students and placement for four students (till Jan. 2016).

- o. Constituted an Industry Advisory Panel for IT department in order to enhance industry relations and for getting help related to syllabus modification, projects, internships, placement, etc.
- p. Worked as Main Coordinator of the Alumni Meet – 2016 (held during 24th and 25th December, 2016)
- q. Worked as Exam Coordinator for the first semester examinations (Mid term and End term) during the academic year 2017-18
- r. Appointed as a member on the Ad hoc BOS in Computer and Information Technology in the faculty of Science and Technology at SRTMU, Nanded from 26th April, 2018 for a period of Two years.

Membership of Professional Bodies

Life member of Institution of Engineers (India)

Life member, IET

IEEE member during 2012-13

Dr. Mrs. J. V. Megha