Suraj D. Kulkarni

26 August 1990

+91-96892 74491

kulkarni.dsuraj26@gmail.com

At. Post-Ekurga, Tal. Dist.-Latur, Maharashtra, India-413 511

About me

Suraj completed C-DAC in Embedded Systems Design.

He has two years of teaching experience at Dr.B.A.T.University, Lonere, Raigad.

He obtained post graduate degree from Savitribai Phule Pune University, Pune.

Key Skills

C, VHDL, Structured Text

DS, AVR, ARM, Cortex-M, Embedded Linux, Device Drivers, RTOS, Zigbee, Wi-Fi, Bluetooth, USB, Microcontroller, W-LAN, CAN, IoT, Android

Keil, MATLAB, Eclipse

Education

C-DAC(Embedded Systems Design)- 2018, Hyderabad

M.E. (VLSI and Embedded System), Savitribai Phule Pune University- 2015; Sinhgad Technical Institute's Smt. Kashibai Navale College of Engineering, Pune; 6.92 CGPA

B.E. (Electronics and Telecommunication Engineering), University of Pune- 2012; Jayawant Shikshan Prasarak Mandal's Rajarshri Shahu College of Engineering, Pune; 62.93%

H.S.C.(Science), Maharashtra State Board of Secondary and Higher Secondary Education, Pune- 2008; Dayanand Science College, Latur, Dist-Latur; 83.83%

S.S.C.(Marathi), Maharashtra State Board of Secondary and Higher Secondary Education, Pune-2006; Janata Vidhya Mandir, Murud, Tal. Dist-Latur;82.13%

Journal

International Journal of Engineering Research and Technology (IJERT), ISSN: 2278- 0181 ('Vehicle Number Plate Recognition System for Theft Detection', Suraj D. Kulkarni, Pallavi S. Deshpande); Volume. 4, Issue. 02, February-2015, Page No.773-775

Conference

K.K.Wagh Institute of Engineering Education and Research, Nashik 422 003; "ePGCON-2014" ,(A Conference for Master of Engineering (Electronics) Students); Suraj D. Kulkarni, Pallavi S. Deshpande, Department of Electronics and Telecommunication Engineering and Bos (Electronics), University of Pune, Pune (March 22-23, 2014)

International Symposium on Engineering and Technology, Pune, India, (Trinity College of Engineering and Research, Pune), 'FPGA Based Automatic Vehicle License Plate Recognition System', International Conference on Design, Manufacturing and Mechatronics (January 9-10, 2014)

Pravara Rural Engineering College, Loni, Ahmednagar 413 736; "ePGCON- 2013";A Conference for Master of Engineering (Electronics) Students, Department of Electronics and Telecommunication Engineering and Bos (Electronics), University of Pune, Pune (March 20 -21, 2013)

JSPM's Rajarshri Shahu College of Engineering, Tathawade, Pune 'Intensive Care Module', Anup V. Kulkarni, Sanket Thalkari, Suraj D.Kulkarni, Sushen R. Gulhane ,National Conference on Emerging Trends in Engineering, Science, Technology and Management-2012,March-2012

aws Innovative Online Conference-Global Edition,18 July 2019

Faculty Development Program

One week, "Software Architecture" held at Dr. Babasaheb Ambedkar Technological University, Lonere (Raigad):29th Sept to 3rd Oct, 2016

Suraj D. Kulkarni

Teaching Interest

Semiconductor Physics, Basic Electronics Engineering, Digital Logic Design, Electronics Devices and Circuits

Research Interest

Embedded Systems Design

Personal Details

- Nationality- Indian
- Category-Open
- Gender-Male
- Hobby- Listening Music
- Languages- Marathi, English
- Marital Status- Unmarried

Strength/ Personal Skill

- Good Team Player
- Can handle pressure
- Strong communication
- Interpersonal skills

Project

- IOT based Vehicle Monitoring System using CAN
- Vehicle Number Plate Recognition System using ARM-7
- Intensive Care Module for Patient Monitoring
- Autonomous Energy Meter

One day National Seminar on "Draft of New National Educational Policy-2019" Conducted at Savitribai Phule Pune University, Pune on 29/06/2019

Workshop

Two Days Workshop on "Stepping towards Problem and Project based Learning: Introductory Workshop", S.G.G.S. Institute of Engineering & Technology, Vishnupuri, Nanded,12-13 July 2019

Dr. Babasaheb Ambedkar Technological University, Lonere and NASSCOM, One Day Workshop on 'Adoption of Product Design Engineer Mechanical- 'Courseware'', Dept. of Computer Engineering, June 15,2017.

Experience

Shri Guru Gobind Singhji Institute of Engineering & Technology Vishnupuri, Nanded (M.S.) PIN 431 606, Asst. Prof. (Dept. of Electronics and Telecomm. Engg.), July 10,2019 to present

Dr. Babasaheb Ambedkar Technological University, Lonere, Dist. Raigad- 402 103 (Established & Funded by Govt. of Maharashtra), Asst. Prof. (Dept. of Electronics and Telecomm. Engg.); July 28, 2016 to Feb 14,2018

Aditya Education Society's Mitthulalaji Sarda Polytechnic College, Beed 431 122, Lecturer (Dept. of Electronics and Telecomm. Engg.); 6 months; July 27, 2016

Extra-Curricular Activity

Participated 'Electronica' (March 17 & 18 ,2011), JSPM's Rajarshri Shahu College of Engineering, Tathawade, Pune; 'Innovision- 2011'

Referee

Dr. Sanjay L. Nalbalwar, Ph.D., Associate Professor and Head Of Department, Department of Electronics and Telecommunication Engineering, Dr. Babasaheb Ambedkar Technological University, Lonere, Dist. Raigad 402 103, +91-94231 78000; *nalbalwar_sanjayan@yahoo.com*

Dr. (Mrs.) Pallavi S. Deshpande, Ph.D. Assistant Professor, Department of Electronics and Telecommunication Engineering, STE's Sinhgad Acedamy of Engineering, Kondhawa, Pune, +91-73503 38133; *pallavisd@rediffmail.com*

Prof. Sushen R. Gulhane, (Ph.D. Pursuing) Assistant Professor, Department of Electronics and Telecommunication Engineering; Dr. D. Y. Patil College of Engineering, Ambi, Talegaon Dabhade, Pune 410 506, +91-86260 77399; *sushenrgulhane1@rediffmail.com*

Declaration

I hereby declare that all information in this curriculum vitae is true and will be responsible for any consequences.

Suraj D. Kulkarni