

Resume

1. **Name** : YASHWANT VASANTRAO JOSHI
2. **Date of Birth** : 22.09.1964
3. **Nationality** : Indian
4. **Martial status** : Married
5. **Address** : Professor, Department of Electronics and
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8. **Research Interests** : Digital Signal Processing, VLSI, Image Processing

9. Academic Qualifications: (B. E. degree onwards)

Sr. No.	Degree	University / Institution	Division / Equivalent	Year of Passing	Specialisation
1.	B. E. (Electronics)	Marathwada University, Aurangabad	First Division with Distinction	1986	Digital Design and Systems Software
2.	M. E. (Electronics)	Marathwada University, Aurangabad	First Division with Distinction	1991	Operating Systems and Computer Aided Design
3.	Ph. D.	Indian Institute of Technology, Delhi	-	1998	Topic : On the design of Infinite Impulse Response (IIR) Digital Notch Filters

10. Employment record:

Sr. No.	Organisation	Post held and pay scale	from	to
1.	SGGS College of Engg. And Tech., Vishnupuri, Nanded 431606	Lecturer (Rs. 2200 to Rs. 4000)	24.11.1986	20.07.1993
2.	SGGS College of Engg. And Tech., Vishnupuri, Nanded 431606	Assistant Professor (Rs. 12000 to Rs.18300)	20.07.1993	19.07.2001
3.	SGGS Institute of Engineering and Technology, Nanded 431606	Professor (CAS) (Rs. 16400-22400, 37400 - 67400, AGP 10000/-)	20.07.2001	12.05.2009
5.	Walchand College of Engineering, Sangli	Director, 37400 -67400, AGP 10000/- Special Pay 3000/-	13.05.2009	01.07.2013
6	SGGS Institute of Engineering and Technology, Nanded 431606	Professor (CAS) (Rs. 37400 -67400, AGP 10000/-) Present Basic Pay 61140/- AGP Rs. 10000/-)	02.07.2013	23.04.2018
7	SGGS Institute of Engineering and Technology, Nanded 431606	Director (Present Basic Pay Fixed Rs. 75000/-)	23.04.2018	Till the date

11. Administrative Responsibilities

Sr. No.	Position	from	to
1.	Head of Department	1.8.2002	10.05.2004
2.	Dean (Academics)	17.07.2004	29.05.2006
3.	Dean (Resource Mobilisation and Finance)	29.10.2007	31.05.2008
4.	Director, Walchand College of Engineering, Sangli	13.05.2009	01.07.2013
5.	Dean (Administration) and In-charge Registrar	16.06.2014	16.06.2016

12. Other Responsibilities

Sr. No.	Post held	from	to
1.	Computer Lab Incharge	1987	1993
		1998	2000
2.	VLSI Lab Incharge (Established)	2000	Oct 2008
3.	TEQIP Nodal Officer Academic Excellence	Dec 2002	May 2006
4.	Media Center Incharge	July 2004	May 2009
5.	M. Tech Coordinator for M. Tech ES VLSI	July 2015	July 2018

13. Recognition of Universities:

- Recognised PG teacher and Ph. D. Guide in SRTMU, Nanded (Electronics and Computer Science), Dr. BAMU, Aurangabad (Electronics Engineering)

Ph. D. Guidance and Completion

Sr. No.	Topic	Name of the Candidate and status
1.	Design and analysis of self secured, scalable, distributed authentication services for Ad-hoc networks (2009)	Mr. V. R. Ghorpade, Ph. D. Awarded
2.	Biomedical Image analysis(2009)	Ms. Varsha Ratnaparkhe, Ph. D. Awarded
3.	Image Compression Techniques with Emphasis on Transform Coding (2009)	Ms. Madhuri Joshi Ph. D. Awarded
4.	Enabling Security Technologies: Watermarking	Mr. Ulhas Gokhale

	Digital Signature and Digital Forensics for Image Authentication (2013)	Ph. D. Awarded
5.	Shallow water ambient noise characterization using passive sonar data (2015)	Mr. Rajveer Shastri Ph. D. Awarded
6.	Computing with Words (CW) on performance Evaluation, Decision making under uncertainty (2017)	Mr. Satish Salunkhe Ph. D. Awarded
7.	Computational Underwater Signal Processing for Tropical Regions (2018)	Mr. Piyush Asolkar Ph. D. Awarded
8.	Electroencephalography based Emotion Analysis and Recognition (2018)	Mr. Narendra Jadhav Ph. D. Awarded
9.	A Novel Approach for Text Recognition in Devanagari Script (2018)	Mr. Madhav Vaidya Ph. D. Awarded
10.	Gesture recognition of Indian Sign Language (2019)	Mrs. Sarita D. Deshpande, Synopsis submitted
11.	Signal Processing (Registered in 2016)	Mr. Mahesh Ladekar Ongoing
12.	Image Processing (Registered in 2012)	Mrs. Jayashri Shinde Ongoing
13.	Embedded systems (Registered in 2017)	Mr. Abhishek Baseshankar Ongoing
14.	Cryptography (Registered in 2013)	Mrs. Aparna Joshi Ongoing
15.	Underwater Signal Processing (Registered in 2016)	Mrs. Jyoti Sadalge Ongoing
16.	EEG signal Processing (Registered in 2017)	Mr. Vinay Kulkarni Ongoing
17.	Image Processing (Registered in 2013)	Mr. Hemprasad Badgujar Ongoing

M.E. /M. Tech. Dissertation Guidance: More than 47 Candidates have completed the dissertation in the areas of Signal Processing, Image Processing, Embedded systems and VLSI

B. E. /B. Tech. Project Guidance: More than 100 Students have completed the Final Year Projects in the areas of Signal Processing, Image Processing, Embedded systems and VLSI

14. Membership of Professional bodies:

- Life member Indian Society for Technical Education (ISTE), New Delhi. (Membership number : LM10002)
- Fellow of IETE (FIETE), Fellow of Institution of Engineers (FIE), Associate Member of IIPA (New Delhi)
- Member, IEEE, Member Signal Processing Society (IEEE), Member Circuits and Systems Society (IEEE).
- Member ACM

15. List of ISTE/QIP/Short term courses attended:

- “Artificial Intelligence and Fault Tolerances in Computers”, ISTE Summer School held from 22nd June to 4th July 1987 at SGS ITS, Indore (MP).
- “MCA Teachers Training programme Module IV”, Sponsored by Department of Electronics (DOE, Govt. of India) held from 8th Dec. 1988 to 18th Jan 1989 at BITS, Mesra, Ranchi

- “CEP advanced course on Information systems” under Informatics Project sponsored by MHRD, Govt. of India, held from 7th to 18th May 1990 at IIT, Mumbai.
- CEP Short Term course on Parallel Processing" held at IIT, Kharagpur from June 27 to July 1, 1994
- “Intensive course on Telematics- A joint telematics group course”, held from 16th to 26th Dec. 1997 at IIT, Delhi.
- “Modular course on High performance VLSI design”, held from 24th to 28th Feb. 1998 at IIT Delhi
- ***Distance Education Program of KRSIT, IIT Bombay – IT606 Embedded systems during January-May 2003 (Audit)***
- Two day course on FPGA design under Xilinx Univ. program, 30th Sept 2003 to 1st Oct 2003 at IIT, Bombay
- Applications of signal processing in networking, 16-17 August, 2004, IIT Mumbai
- Management Capacity and Vision Development : Training for Deans/Heads Nov 16-18,2005 conducted at IIT, Mumbai
- Institutional Capacity building for future, at SGGS IE&T, Nanded conducted by ASCI, Hyderabad Dec. 26-30,2005
- Workshop on Methods of increasing teaching effectiveness, ICT, Mumbai March 13-14,2006
- “DSP Tools and Practices”, held from 12 June 2006 to 17.06.2006 at IIT Kharagpur
- “Embedded systems” held from 19 June 2006 to 24 June 2006 at IIT Kharagpur
- Analog VLSI held from 26 June 2006 to 1st July 2006 at NIT Warangal
- VLSI signal Processing, 22th Dec. to 29th Dec 2006, IIT Kharagpur
- Building a World Class institution, 21-24 Nov. 2007, SIT, Lonavala
- International Training programme on Statistical Inference at UTP Malaysia August 24-25, 2013 UTP Malaysia
- Completed UKIERI-AICTE Further Education Faculty Leadership Programme. Attended all workshops at NITTTR, Chandigarh. CMI Level 5 certification.
- International training programme Design to Delivery, Rutgers University, NJ, USA training programme at LAVASA, 24 to 27 April 2014
- One day National Conference on Leadership in Higher education at ESCI Hyderabad on 4th July 2014
- One day workshop on TEQIP Mentors on 22nd July 2014 organized at New Delhi
- A three day Industrial training program on System Verilog training for faculty organized by Mentor Graphics and Applied Micro at Pune from 10th April to 12th April 2015
- One day workshop on TEQIP performance auditors on 12nd July 2016 organized by NPIU at New Delhi
- One week Management Capacity Enhancement Programme at IIM Trichy Chennai Campus from July 24-29, 2016

- Two day IoT Summit at BVB COE Hubali, Karnatake on 12 and 13 Nov 2016
- One day training programme on Neuro linguistic Programming at Hubballi on 19th Feb 2017.
- Principals Meet organized by e-Yantra project of NMIECT/RUSA at IIT Bombay on 7th March 2017
- Attended VLSID 2018 Conference held at Pune 5th to 10th January 2018, Received the fellowship from Conference organisers
- Attended a One week TEQIP FDP on Universal Human Values at COE Pune Nov 2017

16. List of ISTE/QIP/Short term courses Co-ordinated:

- Two Weeks Computer training programme for Commerce teachers from affiliated colleges of Swami Ramanand Teerth Marathwada University, Nanded
- AICTE sponsored ISTE short term training programme "Digital Signal and Image Processing" conducted from 5th June to 17th June 2000. Seventy participants from engineering colleges all over India attended the course
- AICTE sponsored ISTE short term training programme "VLSI Design using VHDL" conducted from May 24th May 2004 to 5th June 2004. Forty participants from engineering colleges all over India attended the course
- TEQIP COE sponsored One week short term training programme on "Statistical Inference and Linear Algebra" at SGGGS IE&T, Nanded from 16th to 21st Dec 2013 jointly organised with UTP, Malaysia.
- AICTE sponsored two weeks Faculty Development proramme for 50 participants on Multirate signal Processing and Wavelets from 29th June to 10th July 2015
- TEQIP COESIP Sponsored One day workshop on Industry Institute interaction and partnership in VLSI and Embedded domain on 4th August 2015. (Participating industries (Intel Bangalore, APM, Pune, Mentor Graphics, Bangalore, Sankalp Semiconductors Pvt. Ltd. Hubli, SocTronics Hyderabad, Eklakshya VLSI R&D, Pune.

17. Subjects Taught :

Undergraduate Level	Post-Graduate Level
Digital Signal Processing	Advanced Digital Signal Processing
Signals and systems	Advanced Computer Architecture
Network Theory	Adaptive Signal Processing
Communication Systems Analysis	Data Compression
VLSI design	VLSI design
Data structures	Embedded system Design
Unix Operating system	Research Methodology
Embedded system Design	Modern Digital Design using Verilog
C Programming	Statistical Signal Processing
	Advanced Verification Techniques using System Verilog

18. Research Projects

"Design of IIR notch filter chips for removal of single/multiple frequency interference" sanctioned and funded by All India Council for Technical Education, New Delhi, R&D project 1999 Rs. 5.00 Lakhs

Biomedical Texture Analysis sanctioned and funded by All India Council for Technical Education, New Delhi (RPS Scheme 2004) Rs. 8.60 Lakhs

Other Funded Projects of AICTE

Modernisation of Electronics Circuits Laboratory, 2002-03, Rs. 6.00 Lakhs funded by All India Council for Technical Education, New Delhi

TAPTEC project on Centre of Excellence in VLSI design, 2003-5, Rs. 6.00 Lakhs funded by All India Council for Technical Education, New Delhi

DST FIST proposal:

A proposal of strengthening PG laboratory in VLSI is sanctioned by DST, New Delhi for Rs. 47.5 Lakhs.

19. List of International/National Journal Publications

- [1] Y. V. Joshi and S. C. Dutta Roy, "Designs of IIR Digital Notch Filters", *Circuits Systems Signal Processing*, Vol.16, No. 4, pp. 415-427, 1997
- [2] Y. V. Joshi and S. C. Dutta Roy, "Designs of IIR notch filters with different passband gains", *IEE Proc. Vis. Image, Signal Process.*, Vol. 145, pp. 6-10, 1998
- [3] Y. V. Joshi and S. C. Dutta Roy, "Design of IIR multiple notch filters based on allpass filters", *IEEE Trans. Circuits Syst.-II*, Feb. 1999, pp 138-144.
- [4] Y. V. Joshi and S. C. Dutta Roy, "Design of IIR multiple notch filters", *Int. J. Circ. Theor. Appl.*, Vol. 26, no. 5, pp. 499-507, 1998
- [5] Y. V. Joshi, " Design of FIR Filter design by using Adjustable window functions- A Review", *New Vision- A research Journal of SRTMU, Nanded*, 1 (2), 1999, pp. 29-36
- [6] V. Mohan, A. Itagi and Y. V. Joshi, Studies on an electronics analog recurrent neural network with retrieval phase weight adaptation, *International Journal*, 2005
- [7] V. R. Ghorpade, Y. V. Joshi, R. R. Manthalkar (2006). "Trust Evaluation in Mobile Ad Hoc Networks", *The Journal of The Computer Society of India*, Vol 36 No 4, Oct-Dec. 2006
- [8] M. S. Joshi, R. R. Manthalkar and Y. V. Joshi, "Image compression using Curvelet, Ridgelet and Wavelet Transform – A comparative study", *ICGST-GVIP Journal*, ISSN 1687-398X, Vol. 8, Issue III, Oct. 2008, pp 25-34
- [9] V. R. Ghorpade, Y. V. Joshi, R. R. Manthalkar, "Fuzzy Logic based Trust Management Framework for MANET", *DSP Journal*, Volume 8, Issue 1, December, 2008, pp- 37-46
- [10] V. R. Ratnaparkhe, R. R. Manthalkar, Y. V. Joshi, " Texture Characterisation of CT images based on Ridgelet Transform", *ICGST-GVIP Journal*, ISSN 1687-398X, Vol. 8, Issue V, January 2009, pp 43-50
- [11] V. R. Ghorpade, Y. V. Joshi, R. R. Manthalkar, "Efficient group key generation in non homogeneous adhoc networks", *IJSDIA International journal of secure digital information age*, Vol 1, No. 1, June 2009 pp- 45-51

- [12] R. K. Shastri and Y. V. Joshi, "Analysis of Statistical Stationarity Measurement Techniques for Ambient Noise in Shallow Tropical Waters", the MILIT Journal Issue II June 2013, PP 17-22 ISSN 2278-6872.
- [13] U. M. Gokhale and Y. V. Joshi, "Reversible Data Hiding Using Increased Peak Histogram and Block Division" in *International Journal of Multidisciplinary Research and Advances in Engineering (IJMRAE)* Vol.3 No. January 2011, pp.389-398.
- [14] U. M. Gokhale and Y. V. Joshi, "A Fragile Watermarking Algorithm for Strict Image Authentication in the Wavelet Domain" in *CIIT International Journal of Digital Image Processing* Vol. 3, No 10, June 2011, pp. 595-599.
- [15] U. M. Gokhale and Y. V. Joshi, "A semi fragile Watermarking Algorithm based on SVD-IWT for Image Authentication" in *International Journal of Advanced Research in Computer and Communication Engineering* vol. 1, Issue 4, June 2012, pp.213-218.
- [16] U. M. Gokhale and Y. V. Joshi, "Integer Wavelet Transform based Digital Signature Scheme for Image Authentication" in *International Journal of Engineering Research and Industrial Applications (IJERIA)* Vol. 4 No.III (August 2011), pp.231-242.
- [17] U. M. Gokhale and Y. V. Joshi, "Noise Estimation using filtering and SVD for Image Tampering detection " submitted to *International Journal of Engineering Science and Innovative Technology (IJESIT)* vol.2 Issue 1, January 2013.
- [18] S. Salunkhe, Y. V. Joshi, A. W. Deshpande, "Zadeh-Deshpande (ZD) Formalism Revisited: A case study on Students' Academic Performance Evaluation", *Applied Soft Computing*, (under review)
- [19] S. S. Salunkhe, Y. V. Joshi and A. W. Deshpande (2016). Can Fuzzy Relational Calculus Bring Complex Issues in Selection of Examiners into Focus, *Journal of Intelligent Systems, Volume 25, Issue 2*, pp. 85–97. (DOI 10.1515/jisys-2015-0105)
- [20] S. S. Salunkhe, Y. V. Joshi and A. W. Deshpande (2015), "Fuzzy similarity measures as a basis for the selection of examiners", *Journal of Fuzzy Set Valued Analysis, vol. 2015, No. 3*, pp. 245-258. (DOI:10.5899/2015/jfsva-00267)
- [21] S. R. D. Prasad, P. K. Gaikwad and Y. V. Joshi, "Real Time Special Effects generation and noise filtration of audio signal using Matlab GUI", *International Journal on Computational Science & Applications(IJCSA)*, Vol.5, No.6, December 2015, pp. 93-109 DOI:10.5121/ijcsa.2015.5607
- [22] S. R. D. Prasad and Y. V. Joshi, "Unknown System Identification using LMS algorithm ", *Journal of Signal Processing*, Vol 2, No. 1, 2016
- [23] S. R. D. Prasad, S. S. Sutar and Y. V. Joshi, " Optimisation of variants of LMS algorithm for adaptive noise cancellation ", *International Journal of Automatic Control System*, Vol 2, No 1, 2016 PP. 18-31
- [24] S. S. Salunkhe, Y. V. Joshi and A. W. Deshpande (2016). Degree of Certainty in Students' Academic Performance Evaluation, *Journal of Assessment and Evaluation in Higher Education*, (In communication)
- [25] P. Asolkar, A. Das, S. S. Gajre and Y. V. Joshi, "Tropical littoral ambient noise probability density function model based on sea surface temperature" *J. Acoust. Soc. Am.* 140 (5), pp.452-457, November 2016

- [26] N. Jadhav, R.R. Manthalkar, Y.V.Joshi, "Effect of Meditation on Emotional Response: an EEG based study", *Elsevier Journal of Biomedical Signal Processing and control*, Vol 34, April 2017 PP. 101-113.
- [27] M. Vaidya and Y. V. Joshi. "Handwritten Numeral Identification System Using Pixel Level Distribution Features", *In Smart Innovations, Systems and Technologies*, Springer International publishing, Vol. 2, pp. 307-315, 2017, *ISSN 2190-3018*.
- [28] M. Vaidya and Y. V. Joshi, "Marathi Numeral Identification System In Devanagari Script Using Discrete Cosine Transform", *International Journal of Intelligent Engineering and Systems, Japan*, vol. 10, no. 06, pp. 78-86, 2017 *ISSN 2185-3118*.
- [29] M. Vaidya and Y. V. Joshi, "Discrete Cosine Transform Based Feature selection For Marathi Numeral Recognition System", *Advance Intelligence and Soft Computing*, Springer International publishing, 2017. *ISSN 2194-5365*.
- [30] S. B. Tambe, S. S. Gajre, Y. V. Joshi, "Cluster-Based RealTime Analysis of Mobile Healthcare Application for Prediction of Physiological Data," *Journal of Ambient Intelligence and Humanized Computing*, DOI 10.1007/s12652-017-0562-9, *ISSN 1868-5137*, Springer, 2017.
- [31] M. Ladekar, Y. V. Joshi and R. R. Manthalkar, Nonuniform Frequency Sampling Approach to FIR Filter Design: Proceedings of ICCASP 2018, Chapter *in Advances in Intelligent Systems and Computing* · January 2019 and [Computing, Communication and Signal Processing](#), pp 729-738
- [32] A. Joshi, Y. V. Joshi and M. Y. Joshi, "Analysis on Logical Key Hierarchy and Variants for Secure Group Communication", *Computing, Communication and Signal Processing* pp 419-430
- [33] J. Sadalge, A. Das and Y. V. Joshi, "Statistical characterization of channel impulse response at tropical shallow fresh water system", *Computing, Communication and Signal Processing Springer Book (Scopus Indexed)*
- [34] S. D. Deshpande, and Y. V. Joshi, "Invariant Feature Representation in Indian Sign Language for Variant Illumination and Posture", in *International journal of applied engineering research*, vol.13, no. 12, pp.10246-10255
- [35] S. D. Deshpande, and Y. V. Joshi, "Automated Indian Sign Language Recognition Using hybrid Features and FFNN" in *Journal of Advanced Research in Dynamical and Control Systems (JARDCS)*, vol. 10, no. 8, pp. 107-115
- [36] S. D. Deshpande, and Y. V. Joshi, "Incessant Signs recognition via Neoteric classifier based on Ls-SVM and Naïve Bayes with the Aid of Multi Features", in *International journal of Computer Science and Engineering*, vol.6, no .9, pp. 918-928
- [37] A. S. Pande, Y. V. Joshi, M. Y. Joshi (2019) "Analysis on Logical Key Hierarchy and Variants for Secure Group Communication," in *Computing, Communication and Signal Processing. Advances in Intelligent Systems and Computing*, vol 810. Springer, Singapore, https://doi.org/10.1007/978-981-13-1513-8_44, Print ISBN978-981-13-1512-1
- [38] J. V. Shinde, Y. V. Joshi, R. R. Manthalkar (2019), "Intervertebral Disc Classification Using Deep Learning Technique", *Proceedings of the International Conference on ISMAC in Computational Vision and Bio-Engineering 2018 (ISMAC-CVB)*. *ISMAC 2018. Lecture Notes in Computational Vision and Biomechanics*, vol 30. Springer Cham, pp 551-563 (**Book Chapter**) • DOI https://doi.org/10.1007/978-3-030-00665-5_55

20. List of National/ International Conferences

- [1] Y. V. Joshi, T. R. Sontakke and R. S. Bichkar, "Process Control of HTHP Beam Dyeing machine in Textile Industry", *National Conference on Real Time Systems held at SGS ITS, Indore, 1990*
- [2] Y. V. Joshi, T. R. Sontakke and R. S. Bichkar, "Computerised Control of Sizing Machine in Textile Industry", *IEEE TENCON91 (region 10) International Conference held at New Delhi, 1991*, pp. 159-163
- [3] Y. V. Joshi, S. S. Deshpande and S. G. Kahalekar, "Microprocessor based Automatic Power Factor Control", *IEEE TENCON93 (region 10) International Conference held at Beijing (China), 1993*, pp.
- [4] Y. V. Joshi, "Proposed Monitoring and Control System for Textile Industry", *Conference on Textile wet Processing and Weaving held at SGGSC&T, Nanded, 1992*.
- [5] M. H. Shastri, Y. V. Joshi and S. N. Talbar, "Monochrome and Color Image Compression using SUB-band coding", *ICCCD-2000, IIT Kharagpur, 14 to 16 Dec. 2000*
- [6] V. Mohan, A. Itagi and Y. V. Joshi, Studies on an electronics analog recurrent neural network with retrieval phase weight adaptation, 5th International conference on simulated evolution and learning, Japan Oct. 2004
- [7] V. Ratnaparkhe, Y. V. Joshi, R. R. Manthalkar, Texture Analysis in Biomedical Texture Analysis, *NGVIP05, Shimoga, 2005*
- [8] Ghorpade V. R., Dr. Joshi Y. V. (2006). "Managing Public Keys in Ad Hoc Networks: A Self Organized Approach", Technical Symposium at Govt. Engg. College (TySGEa-06), Aurangabad, India pp. 501-507
- [9] Ghorpade V. R., Dr. Joshi Y. V. (2006). "Enhancing Security in Ad Hoc Networks by using Short Secret Shares", International Conference IRIS-06, NEC, Kovilpatti, TN, India pp. 303-314
- [10] Ghorpade V. R., Dr. Joshi Y. V. (2003). "Analysis of Authentication Modes for Ad Hoc Networks", National Conference on Computer Communication and Security (IETE-CCS-04), IETE-Dr.BAMU, Aurangabad, India. pp.203-212
- [11] Ghorpade V. R., Dr. Joshi Y. V. (2003). "Secure Web Cookies", National Convention on Enterprise Management through Information Technology (SEMIT BIG-2003), Computer Society of India, Visakhapatnam, India. pp. 298-305
- [12] Ghorpade V. R., Joshi Y. V., "Trust Evaluation in Ad Hoc Networks," International Conference on Information Security (ICIS-05), PEC, Pondicherry, India. pp. 200-205, 2005
- [13] K. P. Deshmukh, G. N. Shinde, A. V. Nandedkar, Y. V. Joshi,(2004) "Multilevel approach for color image segmentation", Fourth Indian Conference on Computer Vision, graphics and Image processing, 338-344, 2004
- [14] V.R.Ratnaparkhe, R.R.Manthalkar, Y.V.Joshi,(2005) " Texture Analysis in Biomedical Image Processing", *Proceedings of NVGIP-05, 2-3March,JNNCE, Shimoga*
- [15] V.R.Ratnaparkhe, R.R.Manthalkar, Y.V.Joshi, (2006) "Segmentation of Biomedical Images using Wavelet Texture Features", *Technical Symposium, Tsygeca, 06, January 23-24,2006*

- [16] V.R.Ratnaparkhe, R.R.Manthalkar, Y.V.Joshi,(2006) "Wavelet Texture Features of Biomedical Images", *National Conference CSIP, Emerging Trends I Computational Science and Information Processing*, April 1-3,2006
- [17] V.R.Ratnaparkhe, R.R.Manthalkar, Y.V.Joshi,(2006) "Bit Plane Texture Features of Biomedical Images", *National Conference on Biomechanics*, December 15-17,2006
- [18] M. S. Joshi, R. R. Manthalkar, Y V. Joshi (2008), " Color Image Compression using Wavelet and Ridgelet transform", Accepted in the *Fifth IASTED International Conference on Signal Processing, Pattern Recognition, and Applications (SPPRA 2008)*, February 13 – 15, 2008, Innsbruck, Austria
- [19] M. S. Joshi, R. R. Manthalkar, Y V. Joshi, "Joint Compression and Classification For Textures in the Wavelet and Ridgelet Domain", 3rd IEEE International Conference **SITIS 2007**, December 16-19 **2007**, Jiangong Jinjiang Hotel, Shanghai, China
- [20] M. S. Joshi, R. R. Manthalkar, Y V. Joshi , "Comparison Of Image Compression Using Wavelet And Ridgelet Transform", International conference International Conference on Systemics, Cybernetics and Informatics (ICSCI) Jan 08, Hyderabad
- [21] R. K. Shastri and Y. V. Joshi, "Gaussianity Analysis of Ambient Noise in Littoral Tropical Water", RIO Acoustics 2013 IEEE/OES Acoustics in Underwater Geosciences Symposium Centro Empresarial, Rio de Janeiro, 24–26 July 2013
- [22] R. K. Shastri and Y. V. Joshi, "Spectral Analysis of Littoral Water Ambient Noise in the Tropical Region", RIO Acoustics 2013 IEEE/OES Acoustics in Underwater Geosciences Symposium Centro Empresarial, Rio de Janeiro, 24–26 July 2013
- [23] R. K. Shastri and Y. V. Joshi, "Characterization of littoral water ambient noise in time and frequency domain" Selected at Acoustics 2013 organized by Acoustic al Society of India and French Acoustical Society at New Delhi.
- [24] R. K. Shastri and Y. V. Joshi, "Comparative analysis of time frequency representations for littoral water ambient noise from the west coast of India", Selected at Acoustics 2013 organized by Acoustic al Society of India and French Acoustical Society at New Delhi.
- [25] R. K. Shastri and Y. V. Joshi, "Higher Order Statistical Analysis of Underwater Ambient Noise" Selected at International Conference on Intelligent Unmanned Systems, 25-27 September 2013, Jaipur, India.
- [26] S. Nanaware, R. K. Shastri, Y. V. Joshi, A. Das, "Passive acoustic detection and classification of marine mammal vocalizations", International Conference on Communications and Signal Processing (ICCSP), 3-4 April, 2014.
- [27] U. M. Gokhale and Y. V. Joshi, "A new Watermarking algorithm based on Image Scrambling and SVD in the Wavelet Domain" Second International Conference on Advances in Computer Engineering – ACE 2011, held on 25 -26 Aug 2011 at Trivandrum, Kerala, INDIA.
- [28] S. S. Salunkhe, Y. V. Joshi and A. Deshpande, "Fuzzy Similarity measures as a basis for the selection of Examiners", IDS – Intelligent Decision Science at Islamic Azad University, **Dubai**, 4-6 Sept 2015
- [29] N. Khaire and Y. V. Joshi, "Diagnosis of Disease Using Wrist Pulse Signal for Classification of Pre-meal and Post-meal Samples", International Conference on Industrial Instrumentation and Control (ICIC), College of Engineering Pune, India. May 28-30, 2015

- [30] A. Gaygole and Y. V. Joshi, "Fixed Point Error Analysis of FIR Canonic Lattice Filter Structure", IEEE International Conference on Information Processing at the Department of Electronics Engineering Vishwakarma Institute of Technology, Pune, India, 16-19 December 2015
- [31] P. Bharade, Y. V. Joshi and R. R. Manthalkar, "Design and implementation of FIR lattice filter using floating point arithmetic in FPGA", IEEE International Conference on Information Processing at the Department of Electronics Engineering Vishwakarma Institute of Technology, Pune, India, 16-19 December 2015
- [32] M. V. Vaidya and Y. V. Joshi, "Statistical Approach for Marathi Numeral Recognition Using Pixel Distribution Features", IEEE International Conference on Information Processing at the Department of Electronics Engineering Vishwakarma Institute of Technology, Pune, India, 16-19 December 2015
- [33] A. S. Kalambe, Y. V. Joshi, N. Kolhare, "Design of an op-amp as a comparator of 10 bit SplitSAR ADC using cadence tool", IC-SSS-2015, Dec 21-23, MSRIT Bangalore, India, PP.89-94
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- [54] Pande A.S., Joshi Y.V, Joshi M.Y , "Enhanced Strict Binary tree Logical Key Hierarchy Algorithm for Secure Group Communication", *International Conference on Computing in Engineering and Technology(ICCET 2019)* (SCOPUS), Aurangabad

21. Other activities/Responsibilities

- Member of Academic Council, SRTMU, Nanded 2004-2006
- Member of Board of studies in Computer Science, Dr. BAMU, Nanded and Management Science, NMU, Jalgaon also Member RRC – Electronics Engineering NMU Jalgaon (2018 onwards), Member RRC of DBAMU (2018 onwards)
- Member of Expert Panel (MPSC) – syllabus preparation
- Worked on AICTE committee for inspection of Engineering Colleges, AICTE BSNL MOU project, AICTE CII Survey Visit to evaluate the institutes
- Worked on NBA accreditation Committee
- Computer Laboratory Incharge for more than 10 years.
- Member of Institute wide Computer Networking and installation of VSAT
- Organised several training programmes for students
- Resource Person in the various ISTE Short Term Training programmes
- Member of departmental purchase committee
- Chairman, Time Table and Academic Calendar preparation of the Institute
- Coordinator, Computer Development Cell (Responsible for affiliating private computer training institutes, conducting examinations and awarding certificates)
- Worked on interview panels for appointments of Faculty in different engineering colleges appointed by Directorate of Technical Education, Maharashtra State, Mumbai, Subject Expert on University Panel)
- Worked as Dean (Administration) and Incharge Registrar, Member Center of Excellence Coordination Committee, Chairman, Monitoring committee of TEQIP
- TEQIP II Mentor for TEQIP Institute Govt. College of Engineering, Jagdalpur, Chattisgarh
- TEQIP II Performance auditor for institutes in Gujrat(2), Punjab (2) institutes
- NEQIP Performance cum financial auditor of North Eastern state Manipur.
- TEQIP III Performance Auditor and Mentor for Four Institutes
- Examiner for M. Tech. dissertation evaluation for MU, Aurangabad, Mumbai University, Pune University, Sant Gadgebaba Amravati University, Nagpur University in Maharashtra
- Started a new PG programme M. Tech. Embedded systems and VLSI Design from AY 2015-16 with an intake of 18, with major focus on Industrial participation in teaching and learning process. Established a Industry sponsored Laboratory from Mentor Graphics with software support worth Millions of US Dollars
- Member Academic Council and RRC of Electronics Engineering at DBATU, Lonere

23. Foreign Visits

- a. One week - August 2013- For attending a three day international training programme and signing of MoU with UTP Malaysia and NTU, Singapore
- b. One week – Sept 2015 -For attending and presenting a paper in the International conference on Intelligent Decision Science at Islamic Azad University, **Dubai**
- c. One Week – April 2016 – Shanghai, China – For attending and presenting papers in MTS IEEE Oceans 2016
- d. Three weeks – Sept-Oct 2016 – For attending
 - a. Three alumni meets of SGGS Alumni Association in US (North America Chapter) on 17th Sept at New Jersey, 24th Sept at San Jose and 1st Oct at Atlanta.
 - b. International Conference IEEE/MTS Oceans 19th to 23rd Sept 2016. Presented three papers and chaired two sessions as session Chair.
 - c. Visited 8 Universities, infrastructure, Incubation Center, Laboratories, Projects and met a few Faculty and studied the possibility of collaboration with reference to credit transfer, Student and faculty exchange and research collaborations with City University New York, San Jose state University, University of California, San Diego, Oakland University, Detroit, Buffalo University, Indiana University Purdue University, Indiana, Purdue University and Georgia Tech University.

23. Names of the Referees :

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